



Substitute for form 1449A/PTO		<b>Complete If Known</b>	
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> (use as many sheets as necessary)		Application Number	10/749,429
		Filing Date	December 31, 2003
		First Named Inventor	Lyonel Renaud
		Art Unit	2631 2611
		Examiner Name	To Be Determined
		Attorney Docket Number	42P18012
Sheet	1	of	1

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
PO		Ravi Budruk, et al., "PCI Express System Architecture", PC System Architecture Series, MindShare, Inc., Addison-Wesley copyright 2004, ISBN #0-3212-15630-7 (Contents, pages vii-xxxvii; Part Three The Physical Layer - Chapter 11 Physical Layer Logic, pages 397-451; and Chapter 12 Electrical Physical Layer, pages 453-486).	
PO		"PCI Express™ Base Specification Revision 1.0a", April 15, 2003 (Incorporated Errata C1-C67 and E1-E4.17) PCI Express (Contents, pages 3-8; and Section 4.2.7 Clock Tolerance Compensation through Section 4.2.8 Compliance Pattern, pages 200-201)	

Examiner Signature	<i>Phumplus</i>	Date Considered	5/24/07
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\*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication.

<sup>1</sup>Applicant's unique citation designation number. <sup>2</sup>Applicant is to place a check mark here if English language Translation is attached.

Based on PTO/SB/08B (08-03) as modified by Blakely, Solokoff, Taylor & Zeffman (wtr) 08/11/2003.  
Send To: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450